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Sheet 1 of 2

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NO. 1405.1055	APPLICATION NO. 10/034,321
<b>LIST OF REFERENCES CITED BY APPLICANT</b> (Use several sheets if necessary)		FIRST NAMED INVENTOR Souichi OKADA, et al.	
		FILING DATE January 3, 2002	GROUP ART UNIT

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA					
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	AC					
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## FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

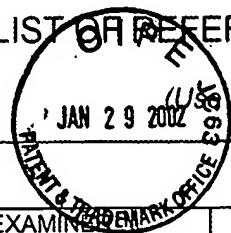
	AM	Henry Kuo, et al., "Architectural Optimization for a 1.82Gbits/sec VLSI Implementation of the AES Rijndael Algorithm", Electrical Engineering Department, University of California, Los Angeles.
	AN	Máire McLoone, et al., "High Performance Single-Chip FPGA Rijndael Algorithm Implementations", DiSip™ Laboratories, School of Electrical and Electronic Engineering, The Queen's University of Belfast, Belfast BT9 5AH, Northern Ireland.

EXAMINER PRAMILA PARTHASAKATHY	DATE CONSIDERED 09/14/05
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	AM	Viktor Fischer, et al., "Two Methods of Rijndael Implementation in Reconfigurable Hardware", Laboratoire Traitement du Signal et Instrumentation, Unite Mixte de Recherche CNRS 5516, Université Jean Monnet, Sainte-Etienne, France.
	AN	Joan Daemen, et al., "AES PROPOSAL: RIJNDAEL", The Rijndael Block Cipher. <a href="http://csrc.nist.gov/encryption/aes/rijndael/Rijndael.pdf">http://csrc.nist.gov/encryption/aes/rijndael/Rijndael.pdf</a> .

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LIST OF REFERENCES CITED BY APPLICANT  (Use several sheets if necessary)		FIRST NAMED INVENTOR Souichi OKADA, et al.	
		FILING DATE January 3, 2002	GROUP ART UNIT 2131

## U.S. PATENT DOCUMENTS

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PP	AG	1 109 350 A1	06/20/01	Europe				X
	AH							
	AI							
	AJ							
	AK							
	AL							

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

PP	AM	M. MCLOONE et al., "High Performance Single-Chip FPGA Rijndael Algorithm Implementations", Cryptographic Hardware and Embedded Systems, 3 <sup>rd</sup> International Workshop, Ches 2001, Paris, France, May 14-16, 2001, Proceedings, Lecture Notes in Computer Science, Berlin: Springer, DE, Vol. 2162, 14 May 2001 (2001-05-14), pages 65-76.
PP	AN	J. DAEMEN et al., "AES Proposal: Rijndael", AES Proposal, XX, SS, 3 September 1999 (1999-09-03), pages 1-45.

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